电平转换芯片（例如 ADG3308）在某些场合可能出问题

Input Output Tech， https://inputoutputtech.com/

2024/5/17

描述

两次在不同的板子遇到：双向电平转换芯片，低电平显著超过 0V。查阅资料发现别人也遇到了。笔者只是说"某些场合可能出问题"，这芯片肯定在很多其他场合是能用的。

资料 1

https://ez.analog.com/interface-isolation/f/q-a/108066/adg3308-output-oscillating-when-low
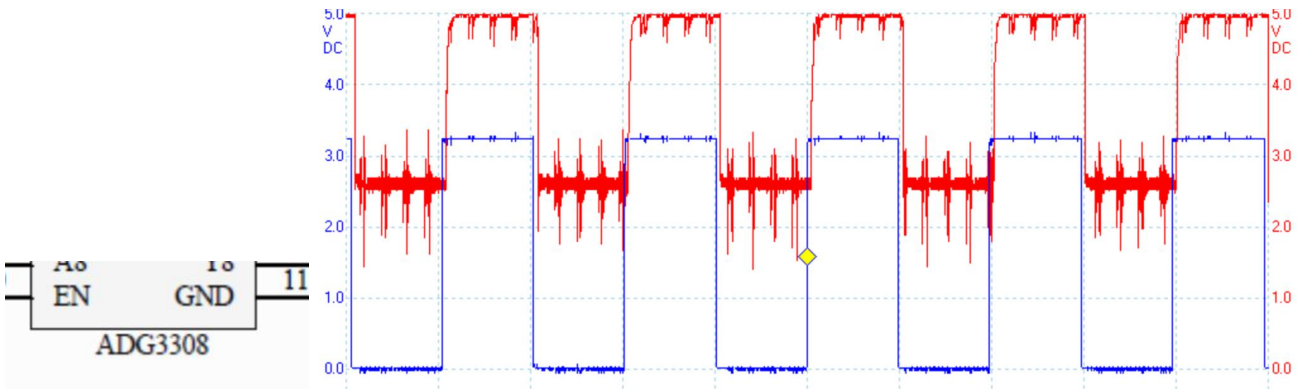
ADG3308 Output oscillating when low

frankziegler *on Mar 4, 2019*

I hadn't noticed the supply sequencing issue. My Vcca 1.8V comes up before the Vccy 3.3 just during power on. My issue is the 3.3V outputs oscillate then they should be low, highs are fine.The traces are 1/4" cmos with .1 caps on both power pins and a solid ground plane beneath.

谷歌翻译：ADG3308 低电平时输出振荡……我的问题是 3.3V 输出振荡然后它们应该很低

资料 2

https://www.edaboard.com/threads/microcontroller-level-translator-not-working.222251/

Q1) Why the ouput pulses does not settle to 0 Volt? Is my circuit correct?
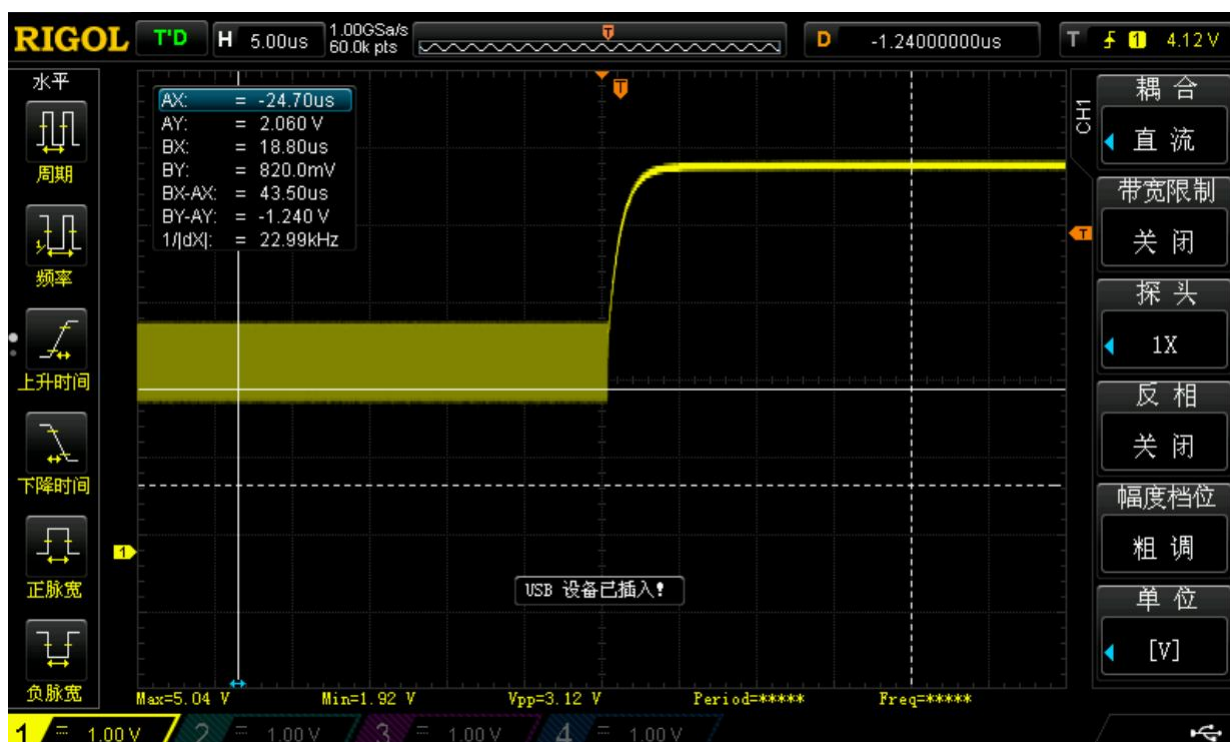
Q2) Why the output pulses have ripples?

Is there a way to solve the problems? TQ.

谷歌翻译：为什么输出脉冲没有稳定至 0 伏？

资料 3

请问有谁用过ADG3308吗？又被虐了！！！

文件结束